

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number 09989765

Filing Date 2001-11-19

First Named Inventor Manfred Bartz

Art Unit 2176

Examiner Name Maikhanh Nguyen

Attorney Docket Number CD01194M

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Art Unit	2176
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
/M.N./	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>
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	17	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 10/20/08; 18 pages	<input type="checkbox"/>
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
EXAMINER SIGNATURE

Examiner Signature	/Maikhanh Nguyen/	Date Considered	12/17/2009
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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/M.N./	1	20020156998	A1	2002-10-24	Casselman, Steven	Entire Document
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/M.N./	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
/M.N./	2	USPTO Final Rejection for 09/975,115 (CD00182) dated 05/12/08; 33 pages	<input type="checkbox"/>
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/M.N./	4	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 07/31/07; 28 pages	<input type="checkbox"/>
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Examiner Name	Maikhanh Nguyen
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/M.N./	26	USPTO Non-Final Rejection for 10/008,096 (CD01212M) dated 06/24/05; 15 pages	<input type="checkbox"/>
	27	USPTO Non-Final Rejection for 10/008,096 (CD01212M) dated 12/12/07; 14 pages	<input type="checkbox"/>
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	33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Monte Mar, filed on 08/29/2001; 46 pages	<input type="checkbox"/>
	34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>
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	45	USPTO Notice of Allowance for 10/113,065 (CD01215M) dated 04/06/2006; 8 pages	<input type="checkbox"/>
	46	USPTO Final Rejection for 10/113,065 (CD01215M) dated 10/26/2005; 17 pages	<input type="checkbox"/>
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/M.N./	48	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Monte Mar, filed on 12/09/1998; 23 pages	<input type="checkbox"/>
/M.N./	49	USPTO Notice of Allowance for Application Number 10/001,477 (CD1203M) dated 05/08/2009; 6 pages	<input type="checkbox"/>

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	2	5399922	B1	1995-03-21	Kiani et al.	Entire Document
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	4	6445211	B1	2002-09-03	Saripella	Entire Document
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/M.N./	9	5258760	B1	1993-11-02	Moody et al.	Entire Document
	10	5563526	B1	1996-10-08	Hastings et al.	Entire Document
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/M.N./	1	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	<input type="checkbox"/>
	2	0450863A2	EP	A1	1991-10-09	Austin	Entire Document	<input type="checkbox"/>
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	5	0639816A2	EP	A1	1995-02-22	McCollum	Entire Document	<input type="checkbox"/>
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	8	1191423A2	EP	A1	2003-02-01	Garnell I Andreu	Entire Document	<input type="checkbox"/>
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/M.N./	1	Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/1994; 10 pages	<input type="checkbox"/>
	2	Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>
	3	USPTO Application Number 09/924,734 (CD00169): "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; Snyder et al., filed on 08/07/01; 28 pages	<input type="checkbox"/>
	4	USPTO Application Number 09/909,045 (CD00170): "Digital Configurable Macro Architecture," Warren Snyder, filed on 07/18/2001; 37 pages	<input type="checkbox"/>
	5	USPTO Application Number 09/909,109 (CD00172): "Configuring Digital Functions in a Digital Configurable Macro Architecture," Warren Snyder, filed on 07/18/2001; 38 pages	<input type="checkbox"/>
	6	USPTO Application Number 09/909,047 (CD00173): "A Programmable Analog System Architecture," Monte Mar, filed on 07/18/01; 60 pages	<input type="checkbox"/>
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/M.N./	8	USPTO Application Number 09/969,311 (CD00175): "Method for Synchronizing and Resetting Clock Signals Supplied to Multiple Programmable Analog Blocks," Bert Sullam, filed on 10/01/01; 57 pages	<input type="checkbox"/>

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/M.N./	9	USPTO Application Number 09/875,599 (CD00180): "Method and Apparatus for Programming a Flash Memory," Warren Snyder, filed on 06/05/2001; 23 pages	<input type="checkbox"/>
	10	USPTO Application Number 09/975,115 (CD00182): "In-System Chip Emulator Architecture," Snyder et al., filed on 10/10/2001; 38 pages	<input type="checkbox"/>
	11	USPTO Application Number 09/953,423 (CD00187): "A Configurable Input/Output Interface for a Microcontroller," Warren Snyder, filed on 09/14/2001; 28 pages	<input type="checkbox"/>
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	24	USPTO Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability," Mar et al., filed on 09/19/2001; 28 pages	<input type="checkbox"/>
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/M.N./	31	USPTO Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture," Warren Snyder, filed on 10/15/2002; 36 pages	<input type="checkbox"/>
	32	USPTO Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 05/09/2005; 1 page	<input type="checkbox"/>
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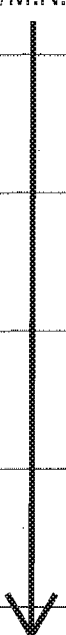
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	29	USPTO Final Rejection for 09/989,778 (CD01172M) dated 12/20/07; 14 pages	<input type="checkbox"/>
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Application Number	09989765
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First Named Inventor	Manfred Bartz
Art Unit	2176
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/M.N./	31	USPTO Non-Final Rejection for 09/989,778 (CD01172M) dated 07/14/08; 24 pages	<input type="checkbox"/>
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	34	USPTO Non-Final Rejection for 09/989,778 (CD01172M) dated 09/18/06; 11 pages	<input type="checkbox"/>
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	2	7117485	B1	2006-10-03	Wilkinson et al.	Entire Document
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/M.N./	9	6678877	B1	2004-01-13	Perry, et al.	Entire Document
	10	6658633	B1	2003-12-02	Devins, et al.	Entire Document
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	13	7206733	B1	2007-04-17	Nemecsek	Entire Document
	14	6371878	B1	2004-04-16	Bowen	Entire Document
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	22	4670838	B1	1987-06-02	Kawata	Entire Document
	23	4939637	B1	1990-07-03	Pawloski	Entire Document
	24	4969087	B1	1990-11-06	Tanagawa, et al.	Entire Document
	25	5313618	B1	1994-05-17	Pawloski	Entire Document
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/M.N./	1	USPTO Notice of Allowance for 09/992,076 (CD01210M) dated 11/13/2008; 15 pages	<input type="checkbox"/>
	2	USPTO Final Rejection for 09/994,601 (CD01170) dated 05/18/2007; 17 pages	<input type="checkbox"/>
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/M.N./	8	USPTO Non-Final Rejection for 09/992,076 (CD01210M) dated 06/01/2005; 20 pages	<input type="checkbox"/>
	9	USPTO Notice of Allowance for 09/994,600 (CD01174) dated 11/12/2008; 35 pages	<input type="checkbox"/>
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	12	USPTO Non-Final Rejection for 09/994,600 (CD01174) dated 05/15/2007; 14 pages	<input type="checkbox"/>
	13	USPTO Final Rejection for 09/994,600 (CD01174) dated 12/08/2006; 14 pages	<input type="checkbox"/>
	14	USPTO Non-Final Rejection for 09/994,600 (CD01174) dated 07/17/2006; 14 pages	<input type="checkbox"/>
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	16	USPTO Non-Final Rejection for 09/994,600 (CD01174) dated 08/23/2005; 13 pages	<input type="checkbox"/>
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	28	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	<input type="checkbox"/>
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	31	ROBINSON, GORDON D; "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	<input type="checkbox"/>
	32	USPTO Non-Final Rejection for 09/994,601 (CD01170) dated 10/04/2007; 20 pages	<input type="checkbox"/>
	33	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>
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	38	USPTO Notice of Allowance for 09/989,777 (CD01208) dated 09/15/2008; 28 pages	<input type="checkbox"/>
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/M.N./	41	USPTO Non-Final Rejection for 09/989,777 (CD01208) dated 09/11/2007; 18 pages	<input type="checkbox"/>
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/M.N./	9	6066961	B1	2000-05-23	Lee et al.	Entire Document
	10	5396245	B1	1995-03-07	Rempfer	Entire Document
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	48	6061511	B1	2000-05-09	Marantz et al.	Entire Document
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/M.N./	1	20040205695	A1	2004-10-14	Fletcher, David	Entire Document
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/M.N./	1	USPTO Notice of Allowance for Application Number 09/930,021 (CD00174) dated 11/26/2004; 4 pages	<input type="checkbox"/>
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Examiner Name	Maikhanh Nguyen
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/M.N./	6	USPTO Non-Final Rejection for Application Number 09/953,423 (CD00187) dated 02/06/2004; 5 pages	<input type="checkbox"/>
	7	USPTO Notice of Allowance for Application Number 09/957,084 (CD00201) dated 05/18/2004; 5 pages	<input type="checkbox"/>
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	9	USPTO Non-Final Rejection for Application Number 09/957,084 (CD00201) dated 08/27/2003; 8 pages	<input type="checkbox"/>
	10	USPTO Final Rejection for Application Number 09/957,084 (CD00201) dated 04/23/2003; 8 pages	<input type="checkbox"/>
	11	USPTO Non-Final Rejection for Application Number 09/957,084 (CD00201) dated 08/23/2002; 6 pages	<input type="checkbox"/>
	12	USPTO Notice of Allowance for Application Number 09/969,313 (CD00227) dated 10/04/2005; 6 pages	<input type="checkbox"/>
	13	USPTO Non-Final Rejection for Application Number 09/969,313 (CD00227) dated 05/06/2005; 9 pages	<input type="checkbox"/>
	14	USPTO Requirement for Restriction/Election for Application Number 09/969,313 (CD00227) dated 03/18/2005; 6 pages	<input type="checkbox"/>
	15	USPTO Notice of Allowance for Application Number 09/969,311 (CD00175) dated 03/01/2005; 4 pages	<input type="checkbox"/>
/M.N./	16	USPTO Non-Final Rejection for Application Number 09/969,311 (CD00175) dated 09/21/2004; 4 pages	<input type="checkbox"/>

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Application Number	09989765
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Examiner Name	Maikhanh Nguyen
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/M.N./	17	USPTO Advisory Action for Application Number 09/969,311 (CD00175) dated 07/21/2003; 2 pages	<input type="checkbox"/>
	18	USPTO Final Rejection for Application Number 09/969,311 (CD00175) dated 04/07/2003; 7 pages	<input type="checkbox"/>
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	21	USPTO Non-Final Rejection for Application Number 09/972,319 (CD00179) dated 09/16/2004; 9 pages	<input type="checkbox"/>
	22	USPTO Notice of Allowance for Application Number 09/972,003 (CD00176) dated 07/14/2004; 4 pages	<input type="checkbox"/>
	23	USPTO Requirement for Restriction/Election for Application Number 09/972,003 (CD00176) dated 05/06/2004; 4 pages	<input type="checkbox"/>
	24	USPTO Non-Final Rejection for Application Number 09/972,003 (CD00176) dated 02/02/2004; 10 pages	<input type="checkbox"/>
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	35	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 08/16/2005; 6 pages	<input type="checkbox"/>
	36	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 03/21/2005; 7 pages	<input type="checkbox"/>
	37	USPTO Notice of Allowance for Application Number 09/975,030 (CD00185) dated 02/06/2006; 7 pages	<input type="checkbox"/>
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/M.N./	39	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 03/29/2005; 13 pages	<input type="checkbox"/>
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	46	USPTO Non-Final Rejection for Application Number 11/125,554 (CD00230C) dated 12/11/2006; 9 pages	<input type="checkbox"/>
	47	USPTO Notice of Allowance for Application Number 09/855,868 (CD00181) dated 04/25/2005; 6 pages	<input type="checkbox"/>
	48	USPTO Non-Final Rejection for Application Number 09/855,868 (CD00181) dated 08/26/2004; 7 pages	<input type="checkbox"/>
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/M.N./	50	USPTO Non-Final Rejection for Application Number 09/887,923 (CD00229) dated 05/25/2004; 7 pages	<input type="checkbox"/>
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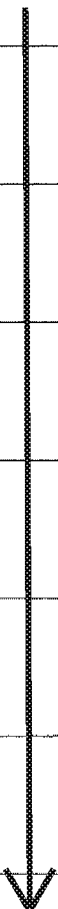
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Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/M.N./ 	1	6934674	B1	2005-08-23	Douezzy et al.	Entire Document
	2	6051772	B1	2000-04-18	Cameron et al.	Entire Document
	3	6016554	B1	2000-01-18	Skrovan et al.	Entire Document
	4	6704889	B1	2004-03-09	Veenstra et al.	Entire Document
	5	5978937	B1	1999-11-02	Miyamori et al.	Entire Document
	6	5317202	B1	1994-05-31	Waizman	Entire Document
	7	6121805	B1	2000-09-19	Thamsirianunt et al.	Entire Document
	8	6211708	B1	2001-04-03	Klemmer	Entire Document

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/M.N./	9	6396302	B1	2002-05-28	New et al.	Entire Document
	10	6204687	B1	2001-03-20	Schultz et al.	Entire Document
	11	5996032	B1	1999-11-30	Baker	Entire Document
	12	5608892	B1	1997-03-04	Wakerly	Entire Document
	13	5808883	B1	1998-09-15	Hawkes	Entire Document
	14	5949632	B1	1999-09-07	Barreras Sr. et al.	Entire Document
	15	6542025	B1	2003-04-01	Kutz et al.	Entire Document
	16	5554951	B1	1996-09-10	Gough	Entire Document
	17	5894243	B1	1999-04-13	Hwang	Entire Document
	18	6081140	B1	2000-06-27	King	Entire Document
/M.N./	19	6104217	B1	2000-08-15	Magana	Entire Document

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/M.N./	20	6286127	B1	2001-09-04	King et al.	Entire Document
	21	6166960	B1	2000-12-26	Marneweck et al.	Entire Document
	22	5982105	B1	1999-11-09	Masters	Entire Document
	23	4740966	B1	1988-04-26	Goad	Entire Document
	24	6643151	B1	2003-11-04	Nebrigg et al.	Entire Document
	25	6003133	B1	1999-12-14	Moughanni et al.	Entire Document
	26	6339815	B1	2002-01-15	Feng et al.	Entire Document
	27	6574739	B1	2003-06-03	Kung et al.	Entire Document
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	29	5969632	B1	1999-10-19	Diamant et al.	Entire Document
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/M.N./	31	6839774	B1	2005-01-04	Ahn et al.	Entire Document
	32	6754765	B1	2004-06-22	Chang et al.	Entire Document
	33	5630057	B1	1997-05-13	Hait	Entire Document
	34	6324628	B1	2001-11-27	Chan	Entire Document
	35	6236593	B1	2001-05-22	Hong et al.	Entire Document
	36	6219729	B1	2001-04-17	Keats et al.	Entire Document
	37	5319771	B1	1994-06-07	Takeda	Entire Document
	38	5378935	B1	1995-01-03	Korhonen et al.	Entire Document
	39	5572719	B1	1996-11-05	Biesterfeldt	Entire Document
	40	5579353	B1	1996-11-26	Parmenter et al.	Entire Document
/M.N./	41	5696952	B1	1997-12-09	Pontarelli	Entire Document


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/M.N./	42	5774704	B1	1998-06-30	Williams	Entire Document
/M.N./	43	5986479	B1	1999-11-16	Mohan, Jitendra	Entire Document
/M.N./	44	6603348	B1	2003-08-05	Preuss et al.	Entire Document

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/M.N./	1	20040153802	A1	2004-08-05	Kudo et al.	Entire Document
	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
	3	20010010083	A1	2001-07-26	Sato	Entire Document
	4	20020133794	A1	2002-09-19	Kanapathippillai et al.	Entire Document
	5	20020065646	A1	2002-05-30	Waldie et al.	Entire Document
/M.N./	6	20020133771	A1	2002-09-19	Barnett	Entire Document

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FOREIGN PATENT DOCUMENTS

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/M.N./	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 09/875,599 (CD00180) dated 05/31/2006; 18 pages	<input type="checkbox"/>
	3	USPTO Final Rejection for Application Number 09/875,599 (CD00180) dated 02/15/2006; 18 pages	<input type="checkbox"/>
	4	USPTO Final Rejection for Application Number 09/875,599 (CD00180) dated 11/21/2005; 16 pages	<input type="checkbox"/>
	5	USPTO Advisory Action for Application Number 09/875,599 (CD00180) dated 06/08/2005; 3 pages	<input type="checkbox"/>
	6	USPTO Final Rejection for Application Number 09/875,599 (CD00180) dated 03/29/2005; 20 pages	<input type="checkbox"/>
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	9	USPTO Final Rejection for Application Number 09/875,599 (CD00180) dated 04/26/2004; 15 pages	<input type="checkbox"/>
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	17	USPTO Non-Final Rejection for Application Number 10/004,039 (CD01222M) dated 04/11/2006; 14 pages	<input type="checkbox"/>
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	44	USPTO Notice of Allowance for Application Number 09/923,461 (CD00192) dated 05/12/2005; 6 pages	<input type="checkbox"/>
	45	USPTO Non-Final Rejection for Application Number 09/923,461 (CD00192) dated 07/16/2004; 6 pages	<input type="checkbox"/>
	46	USPTO Notice of Allowance for Application Number 09/929,891 (CD00226) dated 12/23/2005; 4 pages	<input type="checkbox"/>
	47	USPTO Notice of Allowance for Application Number 09/929,891 (CD00226) dated 06/15/2005; 6 pages	<input type="checkbox"/>
	48	USPTO Non-Final Rejection for Application Number 09/929,891 (CD00226) dated 09/13/2004; 6 pages	<input type="checkbox"/>
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/M.N./	1	4608502	B1	1986-08-26	Dijkmans et al.	Entire Document
	2	6373954	B1	2002-04-16	Malcolm et al.	Entire Document
	3	5900780	B1	1999-05-04	Hirose et al.	Entire Document
	4	6850117	B1	2005-02-01	Weber et al.	Entire Document
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	6	5248843	B1	1993-09-28	Billings	Entire Document
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/M.N./	9	5564010	B1	1996-10-08	Henry et al.	Entire Document
	10	5552725	B1	1996-09-03	Ray et al.	Entire Document
	11	5455731	B1	1995-10-03	Parkinson	Entire Document
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Application Number	09989765
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First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
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	12	USPTO Non-Final Rejection for Application Number 12/218,404 (CD05028C1) dated 09/30/2008; 8 pages	<input type="checkbox"/>
	13	USPTO Application Number 11/644,100 (CD05204): "Differential-to-single ended signal converter circuit and method," Jonathon Stiff, filed on 12/21/2006; 33 pages	<input type="checkbox"/>
	14	USPTO Application Number 11/415,588 (CD05028): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan et al., filed on 05/01/2006; 24 pages	<input type="checkbox"/>
	15	USPTO Application Number 12/218,404 (CD05028C1): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan et al., filed on 07/14/2008; 23 pages	<input type="checkbox"/>
	16	USPTO Notice of Allowance for Application Number 11/132,894 (CD04064) dated 04/26/2007; 4 pages	<input type="checkbox"/>
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/M.N./	18	USPTO Application Number 11/132,894 (CD04064): "Open Loop Bandwidth Test Architecture and Method for Phase Locked Loop (PLL)," Jonathon Stiff, filed on 05/19/2005; 38 pages	<input type="checkbox"/>

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/M.N./	19	USPTO Non-Final Rejection for Application Number 11/322,044 (CD05198) dated 05/04/2009; 18 pages	<input type="checkbox"/>
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	25	USPTO Application Number 11/322,044 (CD05198): "Split charge pump PLL architecture," Jonathon Stiff, filed on 12/28/2005; 19 pages	<input type="checkbox"/>
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	28	USPTO Non-Final Rejection for Application Number 10/305,589 (CD02181) dated 10/07/2003; 6 pages	<input type="checkbox"/>
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/M.N./	30	USPTO Application Number 09/849,164 (CD01035): "Reduced Static Phase Error CMOS PLL Charge Pump," Jonathon Stiff, filed on 05/04/2001; 30 pages	<input type="checkbox"/>
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	34	USPTO Non-Final Rejection for Application Number 10/327,217 (CD02174) dated 04/30/2004; 5 pages	<input type="checkbox"/>
	35	USPTO Miscellaneous Action for Application Number 10/327,217 (CD02174) dated 02/10/2004; 1 page	<input type="checkbox"/>
	36	USPTO Application Number 10/327,217 (CD02174): "Single Ended Clock Signal Generator Having a Differential Output," Richmond et al., filed on 12/20/2002; 27 pages	<input type="checkbox"/>
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	38	USPTO Final Rejection for Application Number 10/871,582 (CD03036) dated 02/01/2006; 5 pages	<input type="checkbox"/>
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/M.N./	41	USPTO Notice of Allowance for Application Number 09/404,891 (CD99054) dated 03/04/2005; 6 pages	<input type="checkbox"/>
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/M.N./	1	USPTO Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants," Gehring et al.; filed on 08/22/2002; 32 pages	<input type="checkbox"/>
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/M.N./	6	USPTO Application Number 09/943,149 (CD01085): "Method for Phase Locking in a Phase Lock Loop," Moyal et al., filed on 08/30/2001; 21 pages	<input type="checkbox"/>
	7	Durham et al., "Integrated Continuous-Time Balanced Filters for 16-bit DSP Interfaces," IEEE, 1993; 6 pages	<input type="checkbox"/>
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/M.N./	17	USPTO Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al., filed on 08/24/2004; 24 pages	<input type="checkbox"/>
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	34	5260979	B1	1993-11-09	Parker et al.	Entire Document
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Art Unit	2176
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/M.N./	42	5043674	B1	1991-08-27	Bonaccio et al.	Entire Document
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/M.N./	1	USPTO Advisory Action for Application Number 11/200,619 (CD04112) dated 05/11/2009; 3 pages	<input type="checkbox"/>
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	25	USPTO Miscellaneous Action for Application Number 09/989,819 (CD01181M) dated 12/14/2001; 1 page	<input type="checkbox"/>
	26	USPTO Application Number 09/989,819 (CD01181M): "System and method for creating a boot file utilizing a boot template," Ogami et al., filed on 11/19/2001; 43 pages	<input type="checkbox"/>
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	38	PCT Written Opinion of the International Searching Authority for PCT/US2005/028793, filed 8/12/2005, mailed 11/19/2007; 7 pages	<input type="checkbox"/>
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	Art Unit	2176	
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U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/M.N./	20	4878200	B1	1989-10-31	Asghar et al.	Entire Document
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/M.N./	31	5811987	B1	1998-09-22	Ashmore, Jr. et al.	Entire Document
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	33	5848285	B1	1998-12-08	Kapusta et al.	Entire Document
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	35	6049223	B1	2000-04-11	Lytle et al.	Entire Document
	36	6058452	B1	2000-05-02	Rangasayee et al.	Entire Document
	37	6134181	B1	2000-10-17	Landry	Entire Document
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	44	6049225	B1	2000-04-11	Huang et al.	Entire Document
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/M.N./	1	20080186052	A1	2008-08-07	Needham et al.	Entire Document
/M.N./	2	20070139074	A1	2007-06-21	Reblewski, Frederic	Entire Document

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	2	Ebeling et al., "Validating VLSI Circuit Layout by Wirelist Comparison;" Sept. 1983; in proceedings of the IEEE International Conference on Computer Aided Design (ICCAD-83); 2 pages	<input type="checkbox"/>
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	4	Ohlrich et al., "Sub-Gemini: Identifying Subcircuits using a Fast Subgraph Isomorphism Algorithm;" June 1993; in proceedings of the 30th IEEE/ACM Design Automation Conference; 7 pages	<input type="checkbox"/>
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Doc description: Information Disclosure Statement (IDS) Filed

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	09989765
	Filing Date	2001-11-19
	First Named Inventor	Manfred Bartz
	Art Unit	2176
	Examiner Name	Maikhanh Nguyen
	Attorney Docket Number	CD01194M

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Signature	/Maikhanh Nguyen/	Date Considered	12/17/2009
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